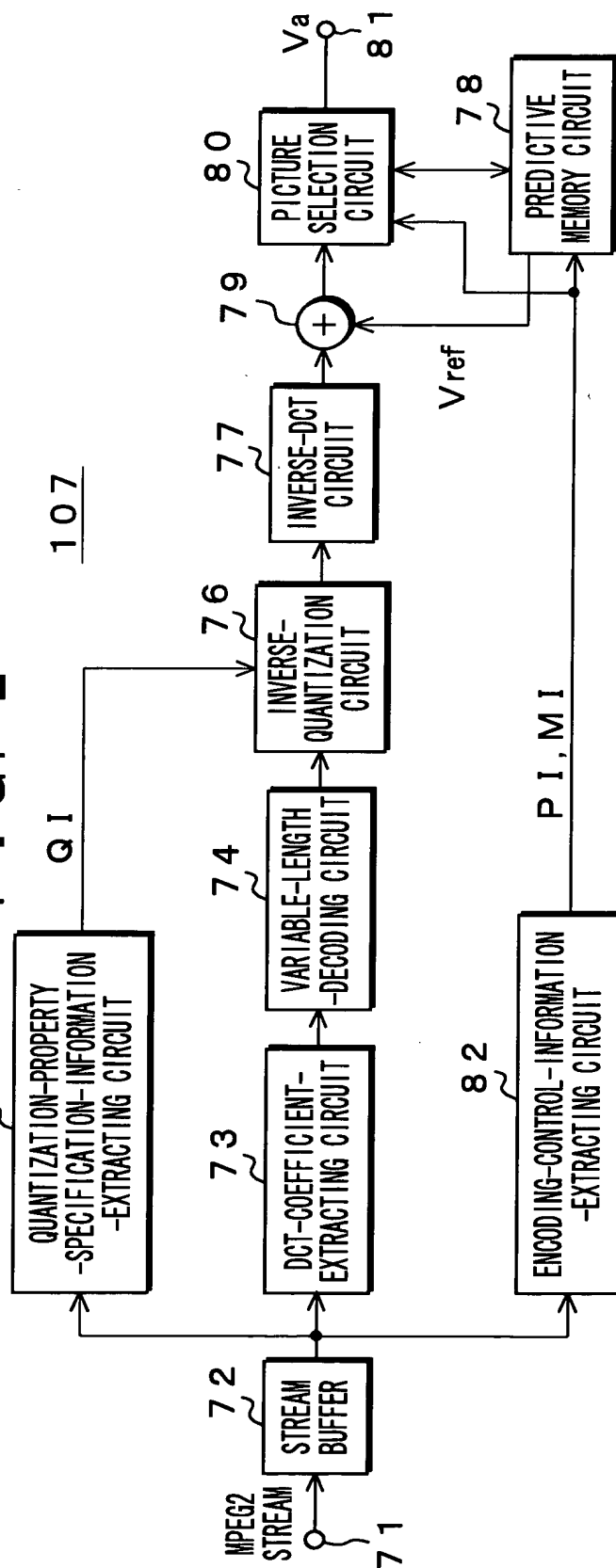


FIG. 2



3 / 1 2

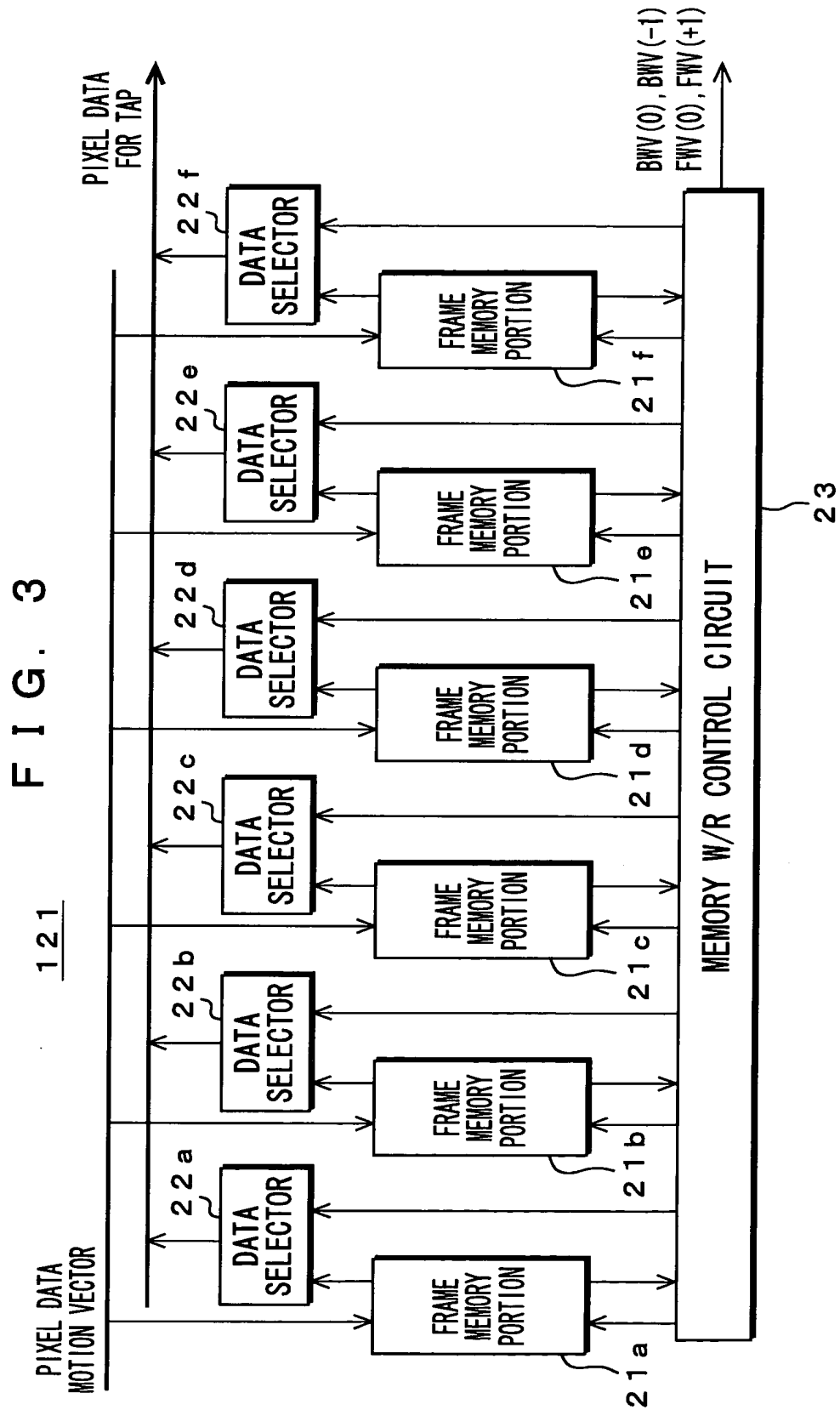
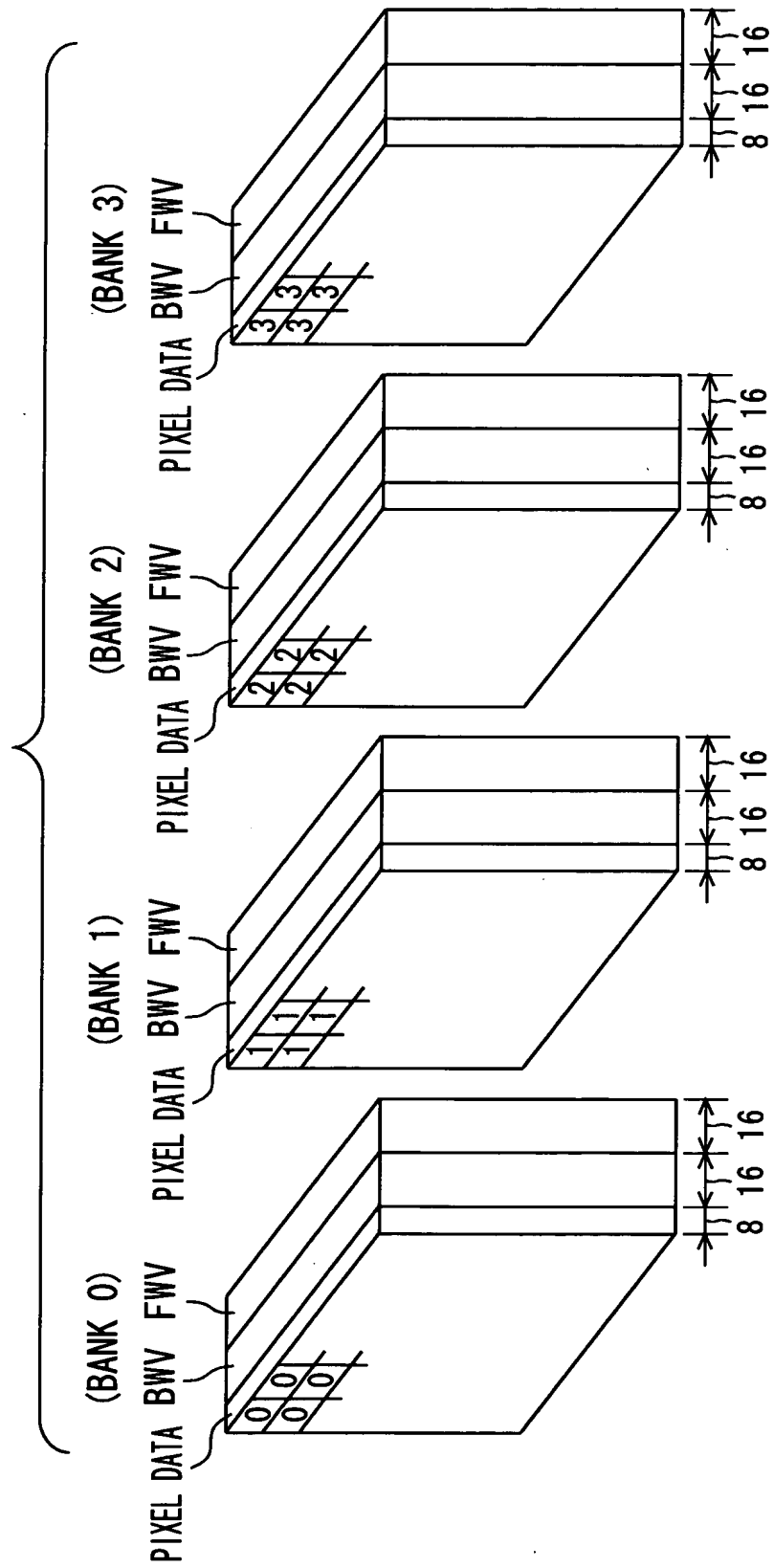


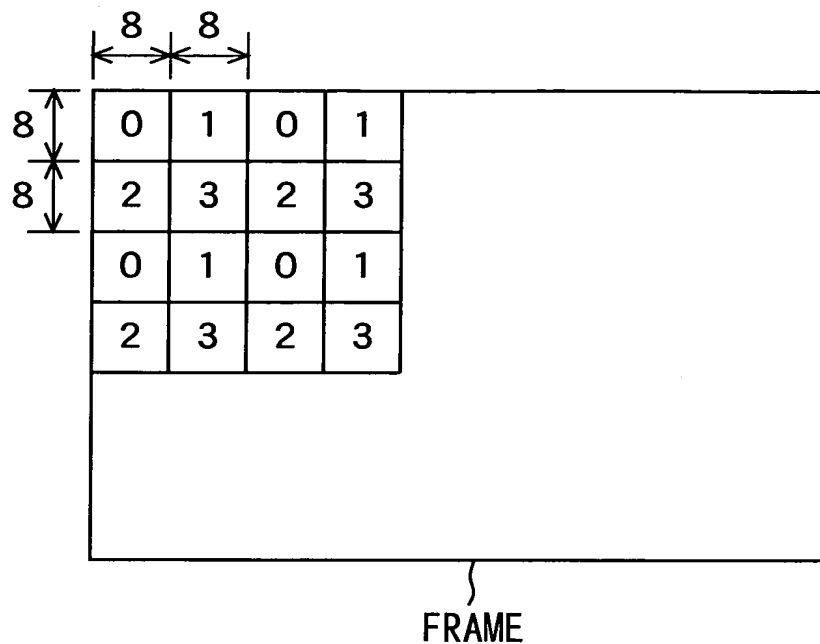
FIG. 4

FRAME MEMORY PORTION

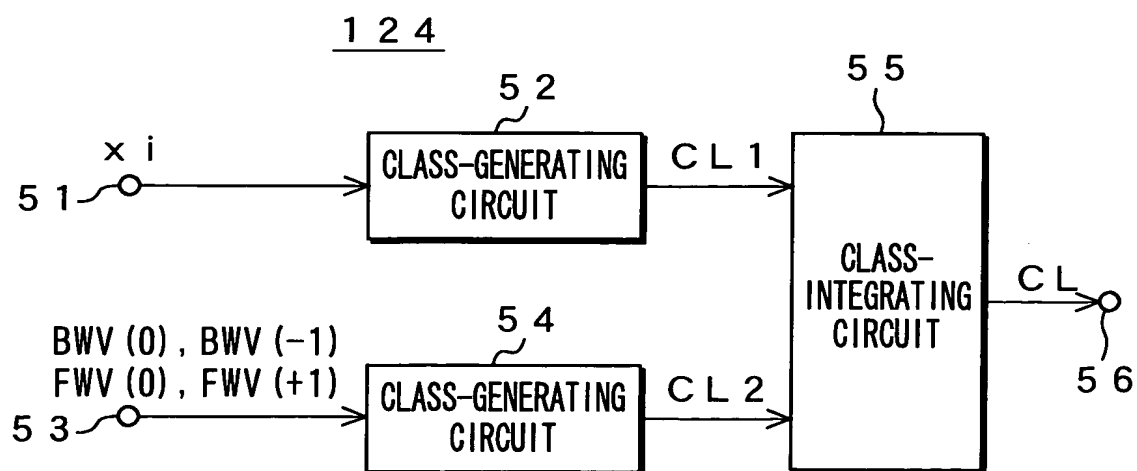


5 / 1 2

F I G . 5



F I G . 9



6 / 1 2

FIG. 6A

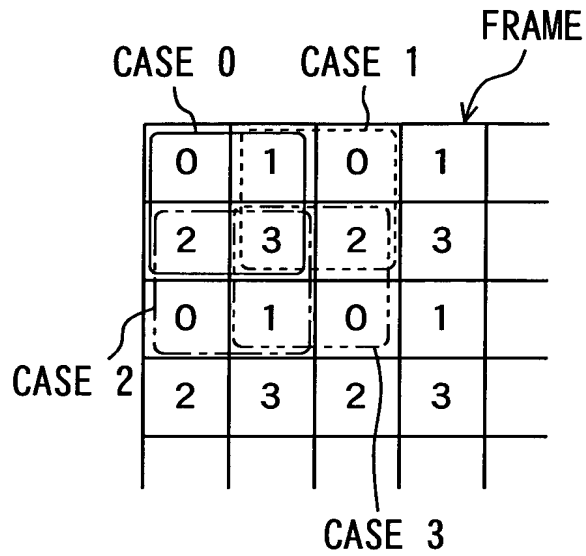
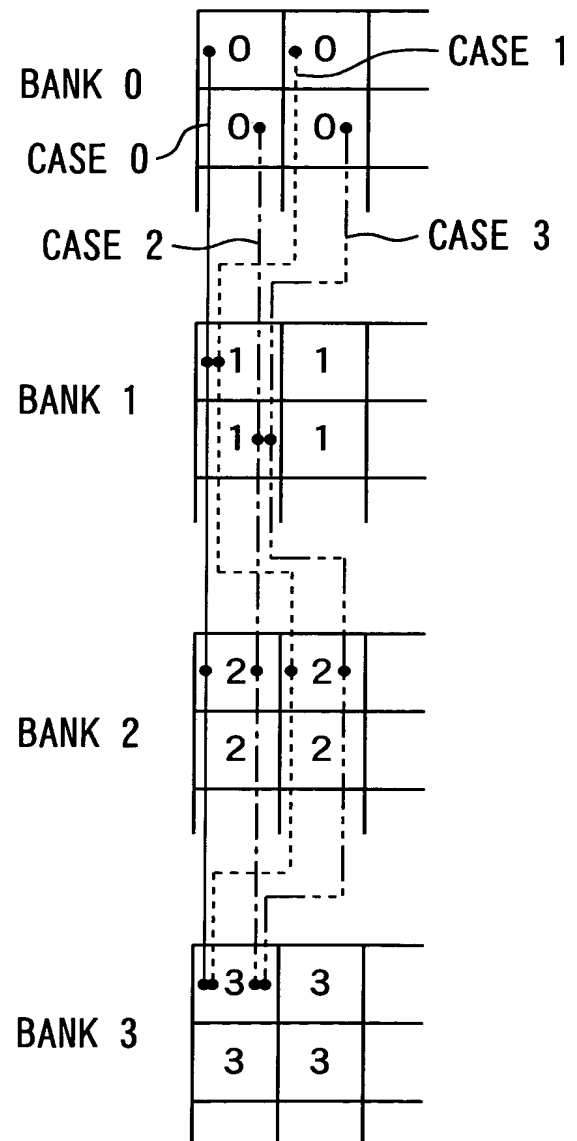


FIG. 6B



7 / 1 2

FIG. 7A
(CASE 0)

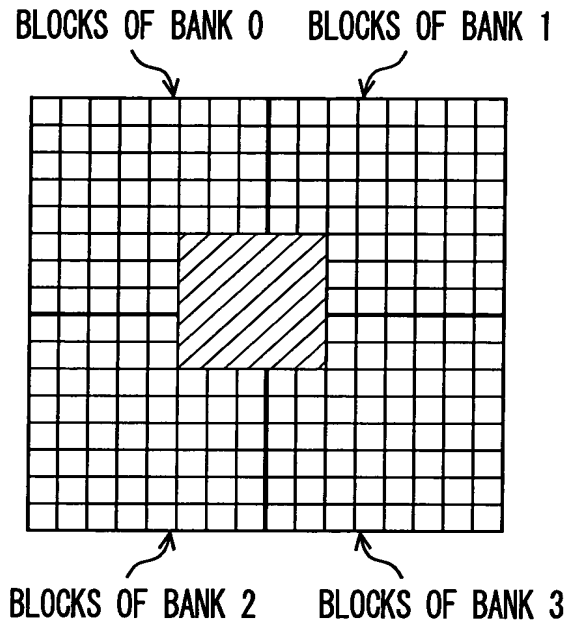


FIG. 7B
(CASE 1)

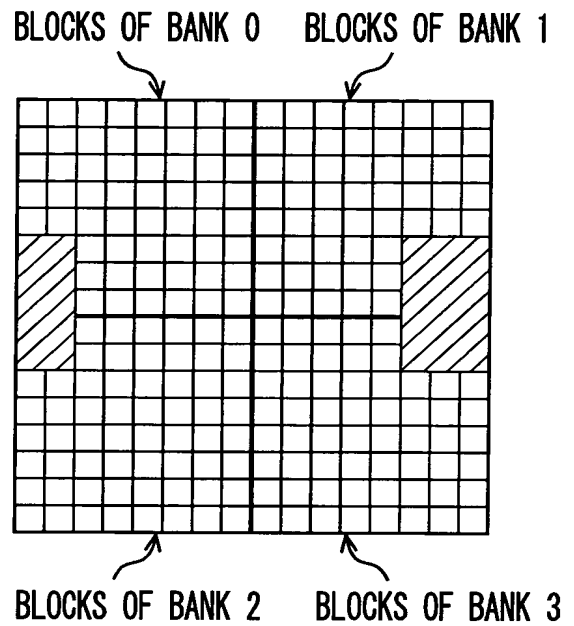


FIG. 7C
(CASE 2)

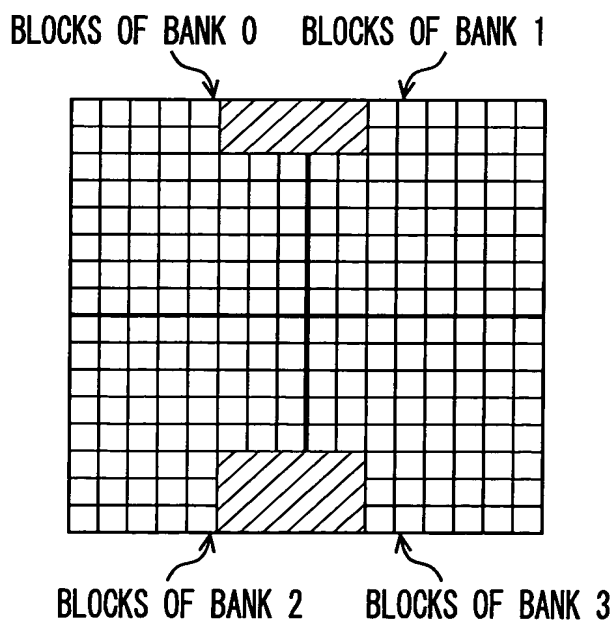
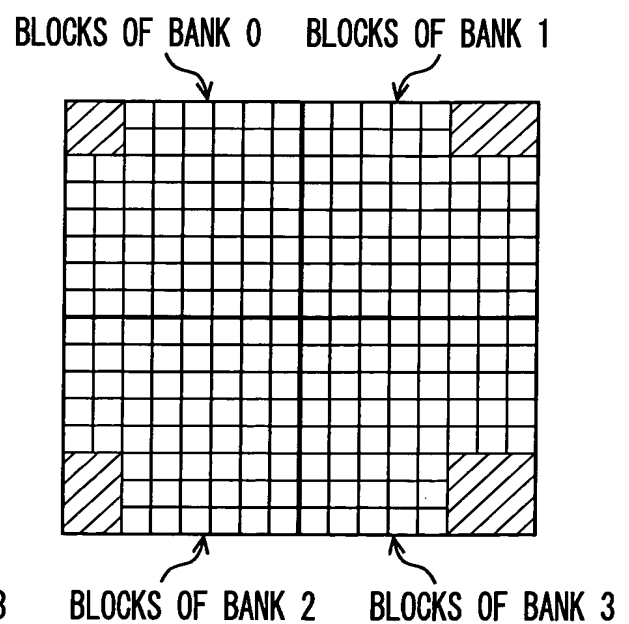


FIG. 7D
(CASE 3)



8 / 1 2

FIG. 8

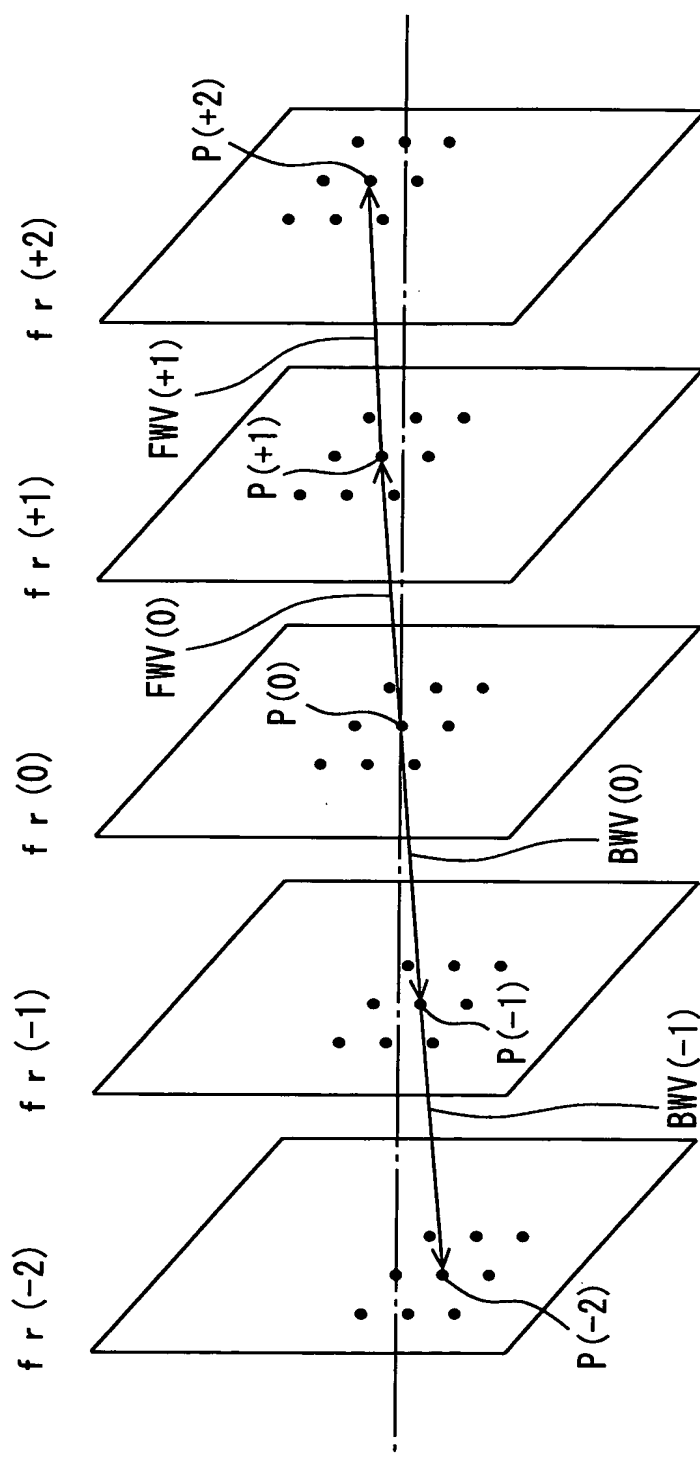


FIG. 10

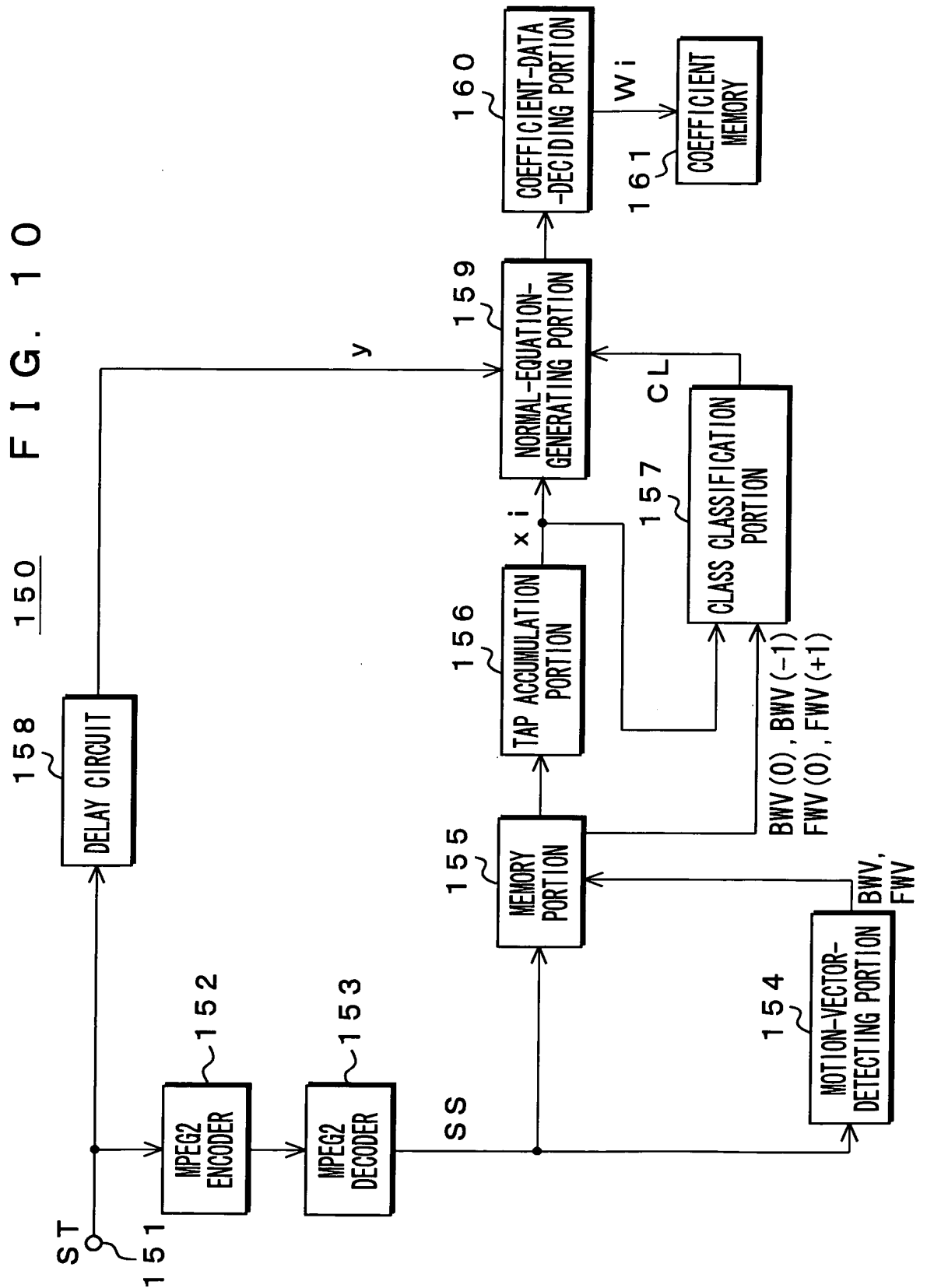
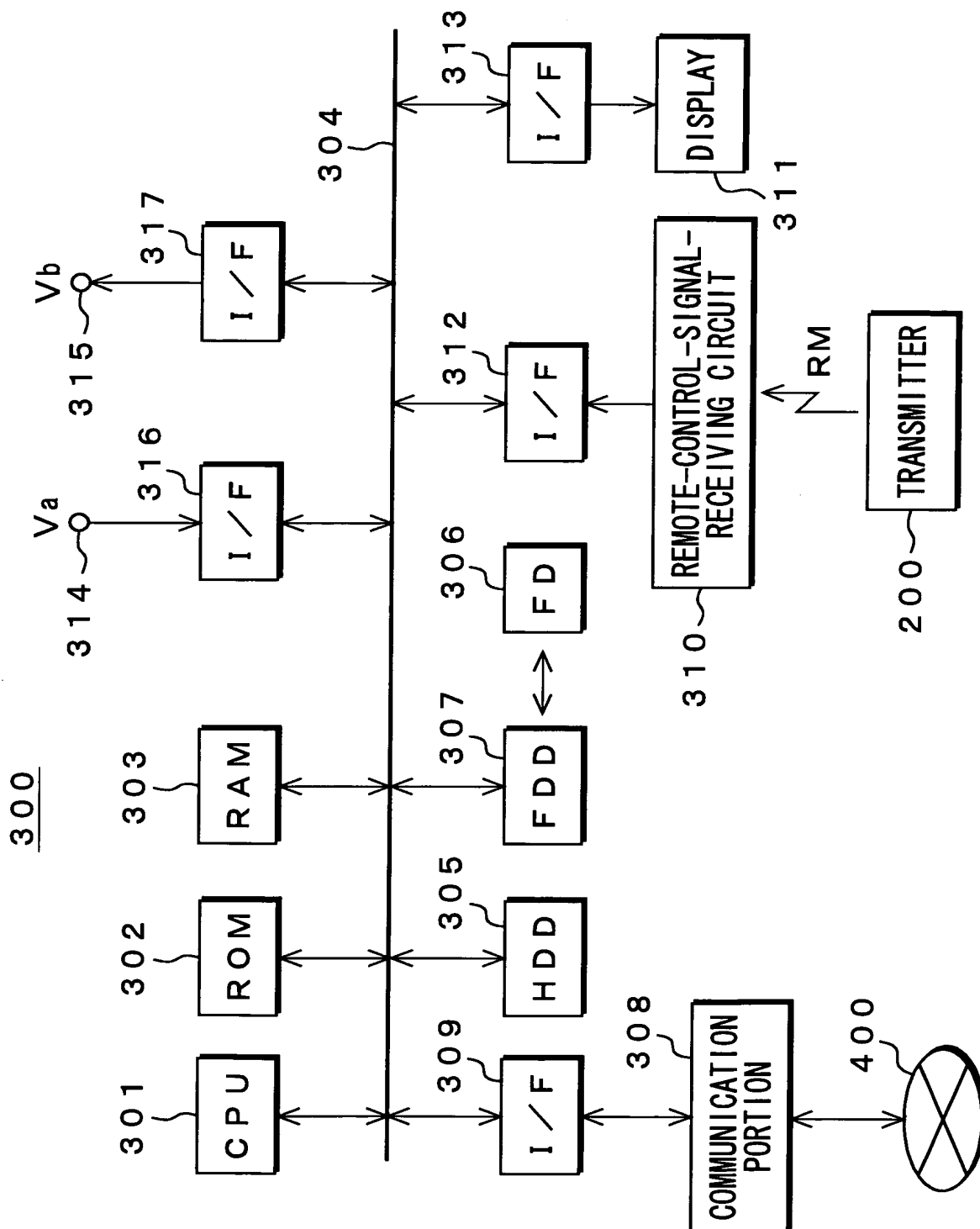
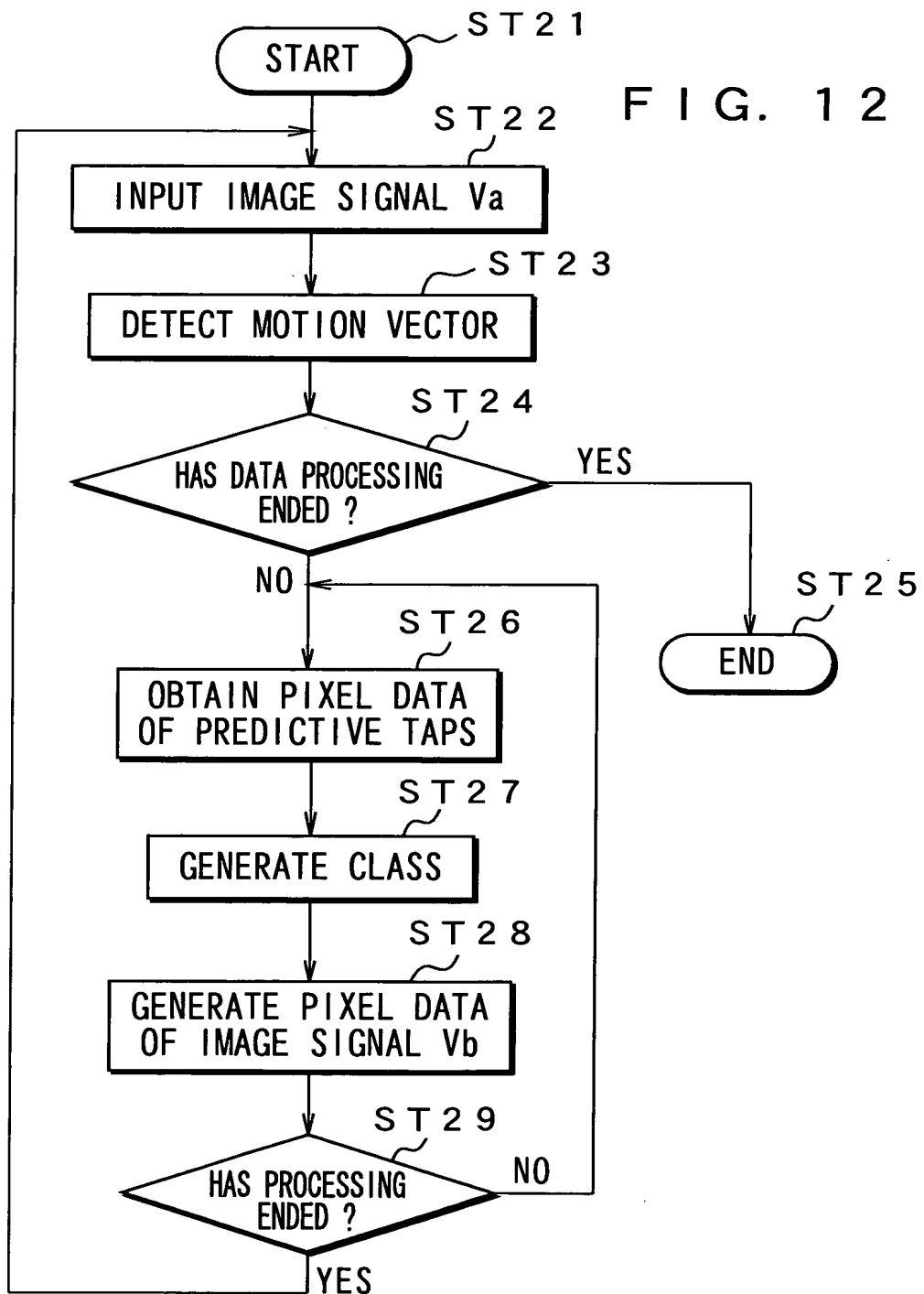


FIG. 11



11 / 12



1 2 / 1 2

FIG. 13

